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PTO/SB/21 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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**TRANSMITTAL  
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	09/854,905
Filing Date	May 15, 2001
First Named Inventor	MOORE, Brian
Group Art Unit	2858
Examiner Name	n/a
Attorney Docket Number	11157-23

**ENCLOSURES (check all that apply)**

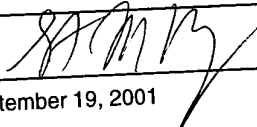
- ☒ Fee Transmittal Form
- ☒ Fee Attached
- ☒ Amendment / Reply
  - ☐ After Final
  - ☐ Affidavits/declaration(s)
- ☐ Extension of Time Request
- ☐ Express Abandonment Request
- ☐ Information Disclosure Statement
- ☐ Certified Copy of Priority Document(s)
- ☐ Response to Missing Parts/Incomplete Application
  - ☐ Response to Missing Parts under 37 CFR 1.52 or 1.53

- ☐ Assignment Papers (for an Application)
- ☐ Drawing(s)
- ☐ Licensing-related Papers
- ☐ Petition
- ☐ Petition to Convert to a Provisional Application
- ☐ Power of Attorney, Revocation Change of Correspondence Address
- ☐ Terminal Disclaimer
- ☐ Request for Refund
- ☐ CD, Number of CD(s)

- ☐ After Allowance Communication to Group
- ☐ Appeal Communication to Board of Appeals and Interferences
- ☐ Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
- ☐ Proprietary Information
- ☐ Status Letter
- ☒ Other Enclosure(s) (please identify below):
  - Return Receipt Post Card
  - Declaration Papers

Remarks

RECEIVED  
SEP 20 2001  
TECHNOLOGY CENTER 3700**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm or Individual name	Stephen M. Beney - Reg. No. 41,563
Signature	
Date	September 19, 2001

**CERTIFICATE OF MAILING**I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date: 

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Signature	<input type="text"/>		

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# BERESKIN & PARR

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PTO/SB/17 (11-00)

Approved for use through 10/31/2002. OMB 0651-0032  
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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## FEE TRANSMITTAL for FY 2001

Patent fees are subject to annual revision.

### Complete if Known

Application Number	09/854,905
Filing Date	May 15, 2001
First Named Inventor	MOORE, Brian
Examiner Name	n/a
Group Art Unit	2858
Attorney Docket No.	11157-23

TOTAL AMOUNT OF PAYMENT (\$ ) 27.00

### METHOD OF PAYMENT

1. ☐ The Commissioner is hereby authorized to charge indicated fees and credit any overpayments to:

Deposit Account Number: 022095

Deposit Account Name: Bereskin & Parr

☒ Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17

☒ Applicant claims small entity status. See 37 CFR 1.27

2. ☒ Payment Enclosed: CHECK # 350

☒ Check ☐ Credit card ☐ Money Order ☐ Other

### FEE CALCULATION

#### 1. BASIC FILING FEE

Large Entity Fee Code (\$)	Small Entity Fee Code (\$)	Fee Description	Fee Paid
101 710	201 355	Utility filing fee	
106 320	206 160	Design filing fee	
107 490	207 245	Plant filing fee	
108 710	208 355	Reissue filing fee	
114 150	214 75	Provisional filing fee	
SUBTOTAL (1) (\$ )			0.00

#### 2. EXTRA CLAIM FEES

Total Claims:  - 20\*\* =  X  =  0.00

Independent Claims:  - 3\*\* =  X  =  0

Multiple Dependent:  =

Large Entity Fee Code (\$)	Small Entity Fee Code (\$)	Fee Description
103 18	203 9	Claims in excess of 20
102 80	202 40	Independent claims in excess of 3
104 270	204 135	Multiple dependent claim, if not paid
109 80	209 40	** Reissue independent claims over original patent
110 18	210 9	** Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$ ) 0.00

\*\*or number previously paid, if greater; For Reissues, see above

### FEE CALCULATION (continued)

#### 3. ADDITIONAL FEES

Large Entity Fee Code (\$)	Small Entity Fee Code (\$)	Fee Description	Fee Paid
105 130	205 65	Surcharge - late filing fee or oath	
127 50	227 25	Surcharge - late provisional filing fee or cover sheet	
139 130	139 130	Non-English specification	
147 2,520	147 2,520	For filing a request for <i>ex parte</i> reexamination	
112 920*	112 920*	Requesting publication of SIR prior to Examiner action	
113 1,840*	113 1,840*	Requesting publication of SIR after Examiner action	
115 110	215 55	Extension for reply within first month	
116 390	216 195	Extension for reply within second month	
117 890	217 445	Extension for reply within third month	
118 1,390	218 695	Extension for reply within fourth month	
128 1,890	228 945	Extension for reply within fifth month	
119 310	219 155	Notice of Appeal	
120 310	220 155	Filing a brief in support of an appeal	
121 270	221 135	Request for oral hearing	
138 1,510	138 1,510	Petition to institute a public use proceeding	
140 110	240 55	Petition to revive - unavoidable	
141 1,240	241 620	Petition to revive - unintentional	
142 1,240	242 620	Utility issue fee (or reissue)	
143 440	243 220	Design issue fee	
144 600	244 300	Plant issue fee	
122 130	122 130	Petitions to the Commissioner	
123 130	123 130	Petitions related to provisional applications	
126 180	126 180	Submission of Information Disclosure Stmt	
581 40	581 40	Recording each patent assignment per property (times number of properties)	
146 710	246 355	Filing a submission after final rejection (37 CFR § 1.129(a))	
149 710	249 355	For each additional invention to be examined (37 CFR § 1.129(b))	
179 710	279 355	Request for Continued Examination (RCE)	
169 900	169 900	Request for expedited examination of a design application	
Other fee (specify) additional 3 claims added			27.00
SUBTOTAL (3) (\$ )			27.00

\*Reduced by Basic Filing Fee Paid

Complete (if applicable)

SUBMITTED BY	Registration No.	Telephone
Name (Print/Type): Stephen M. Beney	(Attorney/Agent): 41,563	(416) 364-7311
Signature: <i>Stephen M. Beney</i>	Date: September 19, 2001	

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5 / Prel. amdt. A  
Draftman's letter  
E. Willis  
10-18-01

Applicant : Brian Moore  
Appl. No : 09/854,905  
Filed : May 15, 2001  
Title : Wireless Radio Frequency Technique Design and Method For  
Testing of Integrated Circuits and Wafers

Grp./A.U. : 2858  
Examiner : N/A

Docket No. : 11157-23

Honorable Commissioner of Patents  
Washington, D.C.  
20231

September 19, 2001

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### AMENDMENT

Sir:

Please amend the above-identified application as follows:

#### In the specification:

Please replace the paragraph beginning at page 28, line 9 with the following rewritten paragraph:

A1 -- Referring now to Figure 26, an embodiment of the synchronization element 66 comprising transistor QC and the coupler 68 comprising capacitor CC is shown. The transistor QC is used to couple energy from the test result signal 34 through capacitor CC to the antenna 50 which is transmitted back to the test unit 12. The source of the transistor QC is connected to the gate of the transistor QC such that the transistor QC acts as a resistor when enabled. The test result signal 34, for a given test state, is coupled to the source of the transistor QC. The transistor QC is enabled by an antenna couple enable signal 170 which is derived from a combination of the state signals S0 or S8 of sequencer 60, such as the logical XNOR of state signals S0 and S8, since these signals have a digital value of '1' when there is no testing being done (i.e. refer to Table 1). When the antenna couple enable signal 170 has a digital logic value of '1', the transistor QC is enabled which allows the test result signal 34 to be applied to the antenna 50 and radiated towards the test unit 12. When the antenna couple enable signal 170 has a digital logic value of '0', the transistor QC is disabled and the test result signal 34 cannot be applied to the antenna 50 and no signal is radiated towards the test